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FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

Attorney Docket No.: 006915 P10 Serial No.: 10/786,410

Applicant(s): Dan MAYDAN, et al. **2825**

Filing Date: 02-24-2004

Group: 41763

**LIST OF PRIOR ART CITED BY APPLICANT**

(Use several sheets if necessary)

**U. S. PATENTS**

Initials	Patent No	Issue Date	Name	Class	Subclass	Filing Date
	US-4,510,672	04-16-1985	YAKURA ET AL.	21	225	04-16-85
	US-4,914,500	04-03-1990	LIU ET AL.	357	67	04-03-90
	US-5,136,969	08-11-92	CHAPMAN	118	305	01-25-91
	US-5,840,589	11-24-1998	WARNER, JR. ET AL.	437	128	06-06-95
	US-5,918,140	06-29-199	WICKBOLDT ET AL.	438	535	06-16-97
	US-6,190,979 B1	02-20-2001	RADENS ET AL.	438	301	06-12-99
	US-6,312,999 B1	11-06-2001	CHIVUKULA ET AL.	438	303	03-29-01
	US-6,475,859 B1	11-05-2002	TEWS ET AL.	438	243	06-13-00
	US-6,500,741 B2	12-31-2002	BUCHANAN ET AL.	438	558	03-28-02
	US-6,506,653 B1	01-14-2003	FURUKAWA ET AL.	438	305	03-13-00
	US-6,566,192	05-20-2003	LIN	438	243	02-07-02
	US 2002/0197806 A1	12-26-202	FURUKAWA ET AL.	438	305	08-16-02
	US 2003/0134469 A1	07-17-03	HORZEL ET AL.	438	249	01-27-03
	US 2004/0077157 A1	04-22-04	CHAKRAVARTHI ET AL.	438	510	10-02-03

Initials

**Other Documents (Title, Author, Date, Pages, Etc., if known)**

Huff, H. and Zeitzoff, Peter-M., "An analytical look at vertical transistor structures", *Solid State Technology*, pp. 59-72, August 2004.

Examiner's Signature: *Agus*Date Considered: *2/24/05*

Initial if reference was considered, whether or not citation with MPEP. Mark through citation if not considered. Include a copy of this citation form with your next correspondence to the Applicant(s).

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